

AUTOMATIC OPERATION CONTROL DEVICE

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Abstract

PURPOSE:To obviate system rise disability due to the intermittent failure of hardware in a system by repeating a rise instruction when the system is not normally raised after generating an automatic rise instruction.

CONSTITUTION:At a reserved rise time, an interruption is generated from a timer part 15 to a microprocessor 1 and power is supplied to respective apparatuses constituting the computer system 20 by the microprocessor 11 through a power supply control part 17. Then, a start command is applied from the microprocessor 11 to the system 20 through a system starting part 14 and system ready monitoring based upon a timer for a fixed period is started. When the normal rise of the system 20 is attained, system ready is reported to the system starting part 14. When system ready report can not be obtained within a timer monitoring time, a start command is outputted again from the microprocessor 11.

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